

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	(Ranjit near Rozardo).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/18 16:42
L2	16	(Ravikrishna near Cherukuri).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/18 16:42
L3	8	(Ranjit near Rozario).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/18 16:42
L4	16	2 or 3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/18 16:42
L5	1589	free adj list	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/18 16:43
L6	472	global adj2 cache	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/18 16:43
L7	1589	free adj list	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/18 16:43
L8	472	global adj2 cache	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/18 16:43
L9	9	L7 same L8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/18 16:43

L10	5685	upper adj threshold	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/18 16:43
L11	24872	lower adj threshold	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/18 16:43
L12	5685	upper adj threshold	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/18 16:43
L13	24872	lower adj threshold	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/18 16:43
L14	4343	L12 and L13	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/18 16:43
L15	4343	L12 and L13	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/18 16:43
L16	18	L7 and L15	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/18 16:43
L17	77842	cache	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/18 16:43
L18	18	L7 and L15	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/18 16:43
L19	77842	cache	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/18 16:43

L20	12	L18 and L19	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/18 16:43
L21	806	L7 and L19	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/18 16:43
L22	806	L7 and L19	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/18 16:43
L23	12	L22 and L15	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/18 16:46
L24	3	4 and 23	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/18 16:47
L25	216875	allocat\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/18 16:47
L26	698	bank near2 cache	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/18 16:48
L27	47	25 same 26	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/18 16:48
L28	3	5 and 27	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/18 16:48
L29	4	8 and 27	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/18 16:48



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- 2 [Hoard: a scalable memory allocator for multithreaded applications](#)  
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- 3 [Caches and Memory Systems: Heterogeneous memory management for embedded systems](#)  
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- 4 [Cache-conscious data placement](#)  
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# 7 Buffer management in shared-memory Time Warp systems

Richard M. Fujimoto, Kiran S. Panesar


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# 8 The performance implications of thread management alternatives for shared-memory multiprocessors

T. E. Anderson, D. D. Lazowska, H. M. Levy

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Hongzhang Shan, Jaswinder Pal Singh

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G. Chen, R. Shetty, M. Kandemir, N. Vijaykrishnan, M. J. Irwin, M. Wolczko

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- 19** Reactive NUMA: a design for unifying S-COMA and CC-NUMA  
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 May 1997 **ACM SIGARCH Computer Architecture News , Proceedings of the 24th annual international symposium on Computer architecture**, Volume 25 Issue 2  
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- 20** Memory system analysis and optimization: Software prefetching for mark-sweep garbage collection: hardware analysis and software redesign  
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